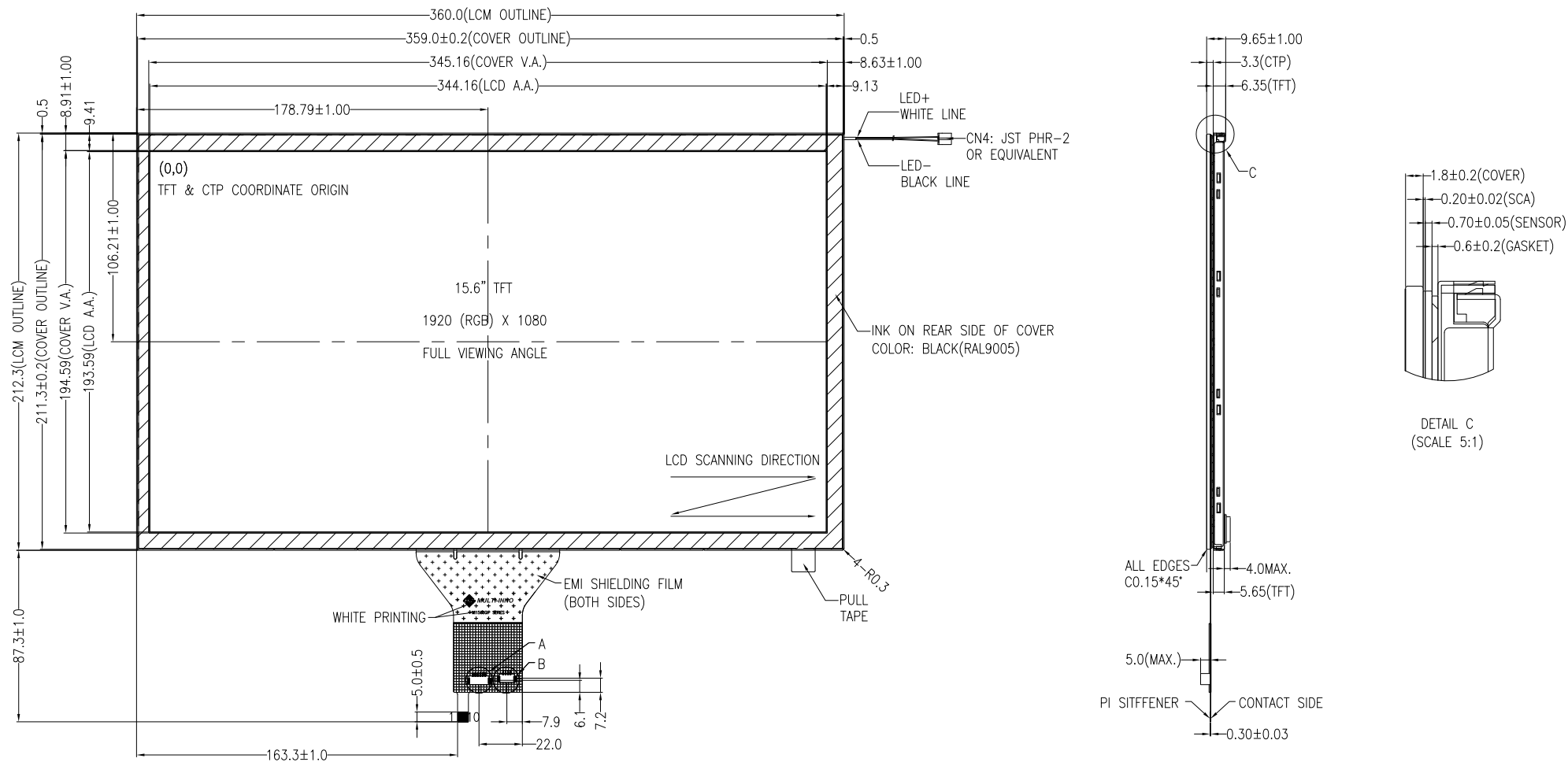




NOTES FOR TFT:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: LCD_VCC=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: HX8036-A52
5. BACKLIGHT TYPE: WHITE LED, If=360mA, Vf=28V~34V
6. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE

NOTES FOR OTHERS:

1. OPERATING TEMP.: -20°C ~ 70°C
2. STORAGE TEMP.: -30°C ~ 80°C
3. SURFACE LUMINANCE: 680 CD/M² (TYP.)
4. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
5. UNMARKED TOLERANCE: ±0.5MM
6. ROHS COMPLIANT

NOTES FOR CTP:

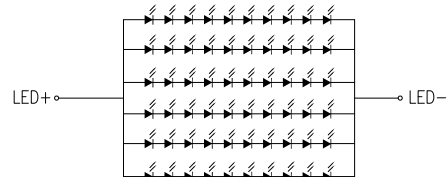
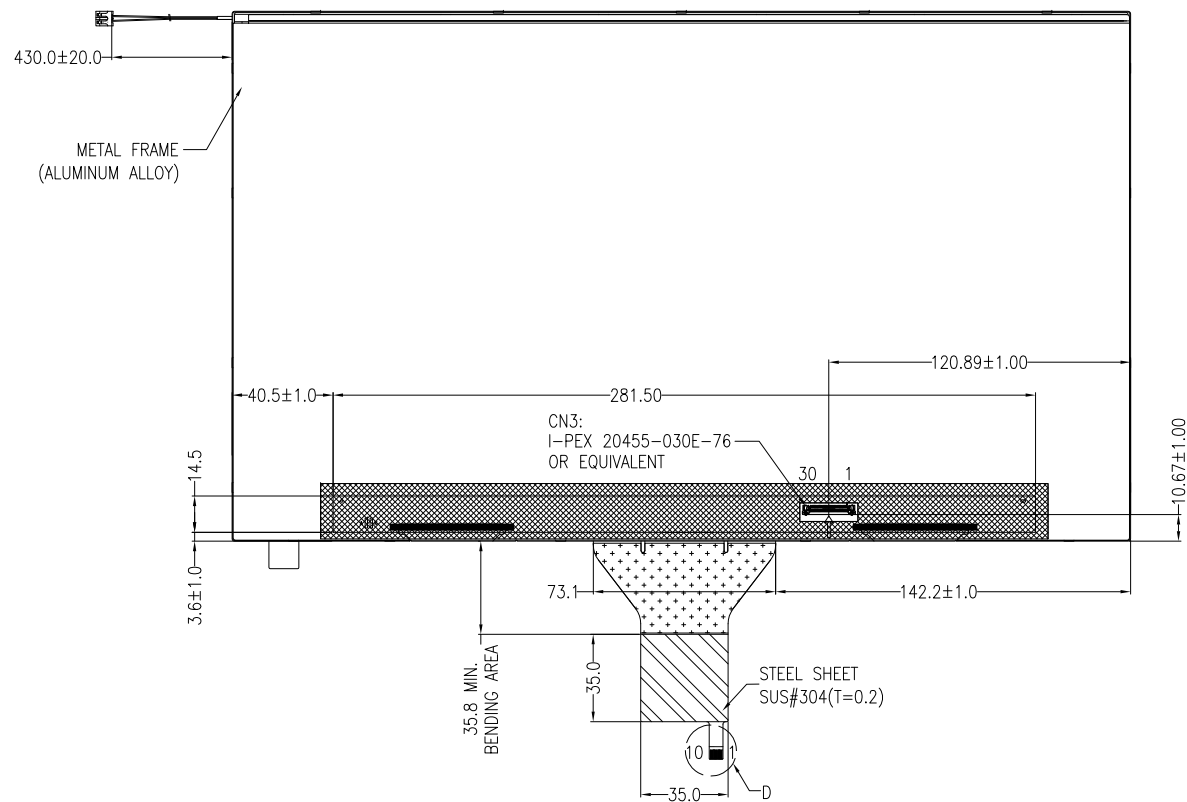
1. 15.6INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCA
3. OPERATING VOLTAGE: IIC_VDD=3.3V, USB_VDD=5.0V
4. DRIVER IC: ILI2511
5. INTERFACE: IIC/USB
6. RESOLUTION: TBD
7. MULTI-FINGER: 10MAX.
8. HARDNESS OF COVER SURFACE: ≥6H (750g)
9. FIRMWARE FILE: TBD



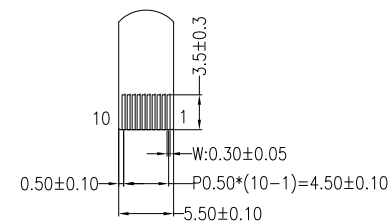
MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA		MODULE P/N:
CHECKED BY:		SCALE : 1/1	MI1560RT-2CP3-A
APPROVED BY:		UNIT: mm	DESCRIPTION:
VERSION NO:	01	SHEET NO.: 1/2	TFT + CTP MODULE

01	FIRST ISSUE	RITA	2025.04.07
VER.	REVISED DESCRIPTION	REVISER	DATE



BACKLIGHT CIRCUIT DIAGRAM



DETAIL D
(SCALE 4:1)

CN1 (CTP USB)		CN3 (LCM)	
PIN	SYMBOL	PIN	SYMBOL
1	USB_VDD (5.0V)	1	NC
2	USB_D-	2	GND
3	USB_D+	3	LANE1 (N)
4	VSS	4	LANE1 (P)
		5	GND
		6	LANE0 (N)

CN2 (CTP IIC)			
PIN	SYMBOL		
1	VSS	9	AUX-CH (P)
2	IIC_VDD (3.3V)	10	AUX-CH (N)
3	IIC_RST	11	GND
4	IIC_SCL	12	LCD_VCC
5	IIC_INT	13	LCD_VCC
6	IIC_SDA	14	NC
		15	GND
		16	GND
		17	HPD

PIN ASSIGNMENT (CTP USB & IIC)			
PIN	SYMBOL		
1	VSS	18	GND
2	USB_VDD (5.0V)	19	GND
3	USB_D-	20	GND
4	USB_D+	21	GND
5	VSS	22	NC
6	IIC_VDD (3.3V)	23	NC
7	IIC_RST	24	NC
8	IIC_SCL	25	NC
9	IIC_INT	26	NC
10	IIC_SDA	27	NC
		28	NC
		29	NC
		30	NC

CN4 (BACKLIGHT)	
PIN	SYMBOL
1	LED+ (WHITE LINE)
2	LED- (BLACK LINE)



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DRAWN BY:	RITA		MODULE P/N:
CHECKED BY:			MI1560RT-2CP3-A
APPROVED BY:		UNIT: mm	DESCRIPTION:
VERSION NO:	01	SHEET NO.: 2/2	TFT + CTP MODULE

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